CERTIFICATE OF MAILING

Shereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to:

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PATENT 36856.636

Applicant: Yoshiyuki TONAMI et al.

Serial No.: 10/087,915

Filed: March 5, 2002

Title: HIGH-FREQUENCY CIRCUIT CHIP AND METHOD OF PRODUCING THE

SAME

Art Unit: 2811

Examiner: O. Nadav

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

## LETTER TO OFFICIAL DRAFTSPERSON

Mail Stop Issue Fee Drawings Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In reference to the subject application, enclosed please find seven (7)(17) sheets of formal drawings (Figs. 1-8) for filing in the above-identified patent application.

Respectfully submitted,

Date: November 19, 2004

Christopher A. Bennett Attorney for Applicants Registration No. 46,710

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